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** CONTINUING DA This applicati ** FOREIGN APPL JAPAN 2001	achida, JAPAN; ATA **********************************	11 02/27/2002 PAT 6,70 **** VERIFIED	08,322				
Foreign Priority claimed yes no 35 USC 119 (a-d) conditions yes no Met after met Verified and Acknowledged Examiner's Signature Initials			SHEETS TOTAL DRAWING CLAIR 14 5			INDEPENDENT CLAIMS 2	
ADDRESS REED SMITH LLP Suite 1400 3110 Fairview Park Falls Church, VA22	Drive						
TITLE Design method and between circuit bloo	apparatus for a semicono	ductor integrated circuit	comprising ch	eckers ve	erifyin	ng the interface	
RECEIVED No	S: Authority has been given in Paper to charge/credit DEPOSIT ACCOUN for following:		1.1 (1.1 (1.1 (1.1 (1.1 (1.1 (1.1 (1.1	☐ All Fees ☐ 1.16 Fees (Filing) ☐ 1.17 Fees (Processing Ext. of time) ☐ 1.18 Fees (Issue) ☐ Other ☐ Credit			